

**Notice of Allowability**

Application No.

10/737,413

Examiner

Jung (John) Hur

Applicant(s)

LIOU, JHY-Y-CHENG

Art Unit

2824

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☐ This communication is responsive to \_\_\_\_.
2. ☒ The allowed claim(s) is/are 1-21.
3. ☒ The drawings filed on 15 December 2003 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some\* c) ☐ None of the:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date \_\_\_\_
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date \_\_\_\_
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☒ Other search history.

  
ANH PHUNG  
PRIMARY EXAMINER

### **DETAILED ACTION**

1. Claims 1-21 are pending in the application.

#### ***Allowable Subject Matter***

2. Claims 1-21 are allowed.

The following is a statement of reasons for the indication of allowable subject matter:

Regarding claim 1, the prior arts of record do not disclose or suggest a non-volatile memory array structure as recited in claim 1, and particularly, gate electrodes of the first transistors in the same first row are coupled with a first sequence word line and gate electrodes of the second transistors in the same second row are coupled to a second sequence word line.

Regarding claim 18, the prior arts of record do not disclose or suggest a semiconductor memory layout as recited in claim 18, and particularly, a plurality of first conductive lines, respectively coupling the gate electrodes of the first transistors in rows and a plurality of second conductive lines, respectively coupling the gate electrodes of the second transistors in rows.

Regarding claim 21, the prior arts of record do not disclose or suggest a non-volatile memory array structure as recited in claim 21, and particularly, a plurality of first word lines, respectively, coupling gate electrodes of the first transistors in a second direction and a plurality of second word lines, respectively coupling gate electrodes of the second transistors in the second direction.

#### ***Conclusion***

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3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Noda (U.S. Pat. No. 5,303,184) discloses a non-volatile memory array structure.

Smayling et al. (U.S. Pat. No. 5,557,569) discloses a non-volatile memory array structure.

Mehrad et al. (U.S. Pat. No. 6,008,516) discloses a non-volatile memory array structure.

Nishizaka, Teiichiro et al. (U.S. Pat. Appl. Pub. No. 2004/0071011) discloses a non-volatile memory array structure.

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jung (John) Hur whose telephone number is (571) 272-1870.

The examiner can normally be reached on M-F 6:30 AM - 3:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Elms can be reached on (571) 272-1869. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

jhh



**ANH PHUNG  
PRIMARY EXAMINER**